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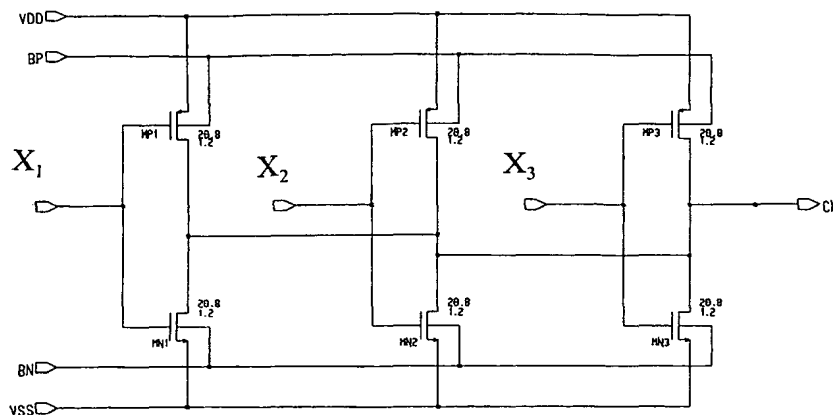
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(54) Title: CIRCUIT ELEMENT



(57) **Abstract:** A circuit element comprising N paired complementary transistors. The transistors are connected to an upper ( $V_{DD}$ ) and lower voltage level ( $V_{SS}$ ), in such a way that the paired transistors operate in subthreshold. N input terminals ( $X_1, X_2, \dots, X_N$ ) are connected to the respective paired transistors. Control terminals (BP, BN) are connected to control input nodes of the transistors. The circuit element provides the possibility of real time configuration between various logic functions with a minimum of transistors and wiring.

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